

## AMENDMENT TO THE SPECIFICATION

Please amend the paragraph on page 11, lines 20 - 29 as follows:

Moreover, on the glass substrate 12  $[[11]]$ , a storage capacitor line 28 extended parallelly to the gate line 16 is provided. The storage capacitor line 28 is electrically connected to each common electrode 17 in the outer peripheral portion and the like of the liquid crystal panel 100, and has a potential equal to that of the common electrode 17, thus holding the voltage in the TFT 19 and the like. Here, the storage capacitor line 28 is arranged so as to cross the central portion in the vertical direction of each pixel region 20, that is, in the "L" shaped bent portion of the signal line 15, at which the signal line 15 is divided into the upper and lower portions 15a and 15b. Above the storage capacitor line 28, the storage capacitor 21b of the foregoing lower pixel electrode 21 and the joint portion 23b are arranged in an overlapped manner.